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List of Patents and Publications
For Applicant's Information
Disclosure Statement
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APPLICANTS:
Kenneth Y. Chiu

U.S. PATENT DOCUMENTS

Exam. Initials	Ref. Des	Document Number	Date	Name	Class	Sub Class	Filing date if appropriate
MB	B1	6,279,099	08/21/2001	Van Hook et al.			
MB	B2	5,933,157	08/03/1999	Van Hook et al.			
MB	B3	5,931,945	08/03/1999	Yung et al.			

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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MB	B4	International Search Report; PCT/US 02/36414; Mailed 22/04/2003					
MB	B5	HOREL T ET AL.: "Ultrasparc-III: Designing Third-Generation 64-Bit Performance" IEEE Micro, IEEE Inc. New York, US, vol. 19, no. 3, May 1999 (1999-05), pages 73-85					
MB	B6	HEALD R ET AL.: "64-Kbyte Sum-Addressed-Memory Cache with 1.6-NS Cycle and 2.6-NS Latency" IEEE Journal of Solid-State Circuits, IEEE Inc. New York, US, vol. 33, no. 11, November 1998 (1998-11), pages 1682-1688					

EXAMINER:

Matthew Bradley

DATE CONSIDERED:

8/6/2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

